IN THE CLAIMS

- 1. Canceled. 2. Canceled. 3. Canceled. 4. Canceled. 5. Canceled. 6. Canceled. 7. Canceled. 8. Canceled. 9. Canceled. 10. (Currently Amended) A bus agent comprising: a plurality of data signal inputs to receive a plurality of data signals;
 - a plurality of data signal inputs to receive a plurality of data signals;

 a plurality of strobe inputs to receive a plurality of strobe signals;

 a state machine coupled to receive said plurality of strobe signals and to

generate therefrom a plurality of clock signals, wherein said state machine is to receive two complementary strobe signals and to generate therefrom four non-overlapping clock signals, wherein said four non-overlapping clock signals have a sequential active period and The bus agent of claim 8 wherein said four non-overlapping clocks have a substantially similar active period; and,

produced by said state machine to detect glitches on one or more of said plurality of strobe signals and to generate an error signal if one of said plurality of clocks is in an incorrect state after a delay duration, wherein said strobe glitch detection circuit is coupled to receive two clock signals of said four non-overlapping clock signals, and further wherein said strobe glitch detection circuit is to generate the error signal if a first one of said two clock signals is in the incorrect state at a time determined from the other one of said two clock signals, and wherein said delay duration is less than a duration of said active period.

- 11. Canceled.
- 12. Canceled.
- 13. (Currently Amended) A bus agent comprising:

a plurality of data signal inputs to receive a plurality of data signals, wherein said plurality of data signals comprise a plurality of sets of data signals, each set having an associated strobe and an inverted strobe signal;

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Docket No. 42390.P8929

a plurality of strobe inputs to receive a plurality of strobe signals;

a state machine coupled to receive said plurality of strobe signals and to
generate therefrom a plurality of clock signals; and

a strobe glitch detection circuit to monitor said plurality of clock signals produced by said state machine to detect glitches on one or more of said plurality of strobe signals and to generate an error signal if one of said plurality of clocks is in an incorrect state after a delay duration,

wherein said strobe glitch detection circuit comprises a plurality of strobe glitch detectors, each of said plurality of strobe glitch detectors being coupled to receive a first clock signal and a second clock signal from said plurality of clock signals and to generate a glitch indicator signal if said second clock signal is in an incorrect state after a predetermined duration measured from a transition of the first clock signal.

The bus agent of claim 12 wherein each glitch detector comprises:

a delay circuit to receive said first clock signal and to generate a delayed first clock signal; and

a latch clocked by said delayed first clock signal, said latch being a falling edge clock triggered latch, said latch having a data input coupled to receive said second clock signal and to generate an output error signal if said second clock signal is absent when a falling edge of the delayed first clock signal clocks the latch.

14. (Original) The bus agent of claim 13 wherein said output error signal is further coupled to said latch to retain the output error signal in an active state until a reset signal is received.

- 15. (Original) The bus agent of claim 14 further comprising:
 - a bus controller to retry a transaction during which a strobe glitch is detected in response to detection of said strobe glitch.
- 16. (Original) The bus agent of claim 15 wherein said bus controller is also, in response said output error signal, to stop sending additional bus requests, to disregard data received in conjunction with said transaction, and to reset each glitch detector.
- 17. (Original) The bus agent of claim 16 wherein said bus controller is to reset each glitch when no further strobes are outstanding.
- 18. Canceled.
- 19. Canceled.
- 20. Canceled.
- 21. Canceled.
- 22. Canceled.
- 23. Withdrawn.

- 24. Withdrawn.
- 25. Withdrawn.
- 26. Withdrawn.
- 27. Withdrawn.
- 28. Canceled.
- 29. Canceled.
- 30. Canceled.